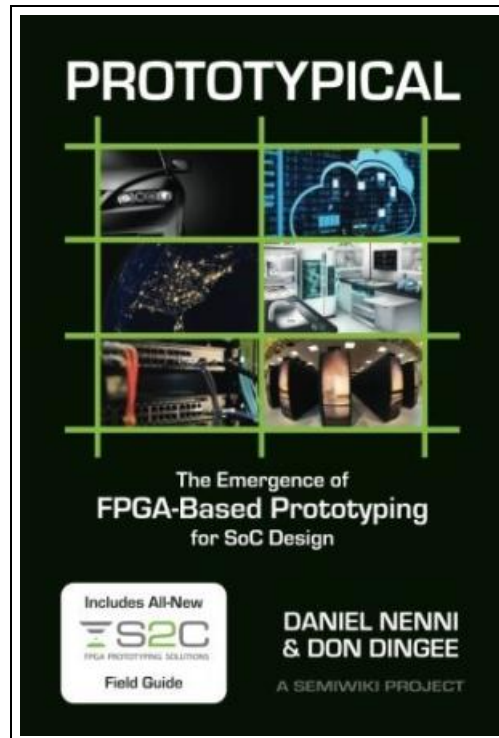# Prototypical: The Emergence of FPGA-Based Prototyping for Soc Design (Paperback)

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Reviews

Simply no terms to explain. I am quite late in start reading this one, but better then never. Its been written in an remarkably easy way and is particularly merely soon after i finished reading this book where basically changed me, affect the way i really believe.
(Prof. Jedediah Kuhic DVM)

PROTOTYPICAL: THE EMERGENCE OF FPGA-BASED PROTOTYPING FOR SOC DESIGN (PAPERBACK)

Createspace Independent Publishing Platform, 2016. Paperback. Condition: New. Language: English . Brand New Book ***** Print on Demand *****. The first half of PROTOTYPICAL is a concise history of FPGA-based prototyping. We go back to the beginning, briefly introducing the debut of the Altera EP300 in 1984 and the Xilinx XC2064 in 1985. We then discuss the tipping point for what would become FPGA-based prototyping: the introduction of the Quickturn Systems RPM in May 1988. Strictly speaking, the RPM was an FPGA-based hardware emulator, but it set the stage for a radical change in chip development methodology. Intel took the Quickturn technology and put the P5 microarchitecture through its paces on a 14-machine cluster, running a killer demo in 1991 and ultimately releasing the Pentium microprocessor in 1993. From there, while the large EDA firms scuffled over bigger and bigger hardware emulation capability, several academic teams started deploying FPGAs for reconfigurable computing and rapid prototyping. These teams were looking for lower cost ways to prove out algorithms and chip designs. It was during this period issues of FPGA interconnect and synthesis partitioning were uncovered and addressed - and just in time, as ARM7TDMI synthesizable cores appeared in 1997. We then launch into chapters with brief timelines of three of the major firms in FPGA-based prototyping: S2C, Synopsys, and Cadence. We close the first half with a look at where FPGA-based prototyping is headed, including how it can help application segments such as automotive, wearables, and the IoT - three segments we believe will see an increasing number of design starts as new players seek to optimize and differentiate their software through chip design. The second half of PROTOTYPICAL is an all-new Field Guide titled Implementing an FPGA Prototyping Methodology authored by the teams at S2C. It looks at when design...

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