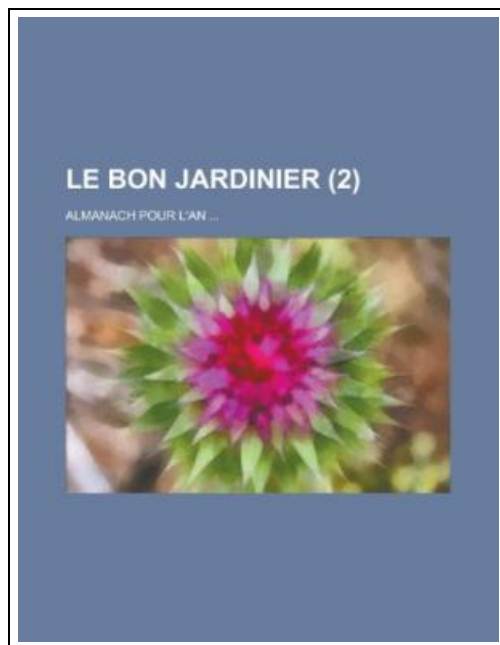


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RareBooksClub. Paperback. Book Condition: New. This item is printed on demand. Paperback. 28 pages. Original publisher: Aberdeen Proving Ground, MD: Army Research Laboratory, 2005 OCLC Number: (OCoLC)707925313 Subject: Weather -- Models. Excerpt: . . . Appendix A. Methods Used for Serial Tuning A. 1 Introduction This appendix deals with optimizations at the processor level (serial tuning). The remainder of this appendix will cover the following topics: 1. The way in which the code interacted with the memory system, in particular with the stream buffers. 2. Cache effects. 3. Translation lookaside buffers (TLBs). 4. Methods used to reduce the time spent on integer arithmetic. 5. Large page sizes. In addition to the approaches to tuning and parallelization discussed in the remainder of this appendix, a number of fairly standard techniques were used to improve the performance of this code. The majority of these techniques were used to eliminate redundant calculations (especially those involving the exponentiation operator). In general, the savings in any one routine was small, but the combined savings amounted to 15 - 20 min for a 6-hr run. A. 2 The Way in Which the Code Interacted With the Memory System, in Particular With the Stream Buffers One key aspect of the IBM SP with Power 4 processors is the ability of the processor to stream data into the caches from main memory and on up to the processor. If each L3 cache miss was handled individually as it happened, most of the memory bandwidth would be unusable. Instead, the processor attempts to stream the data into the L3 cache and from there into the processor whenever possible. While prefetch instructions can help to improve the efficiency of this process, the key component is the group of eight stream buffers. This means that..



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