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BANDWIDTH EFFICIENT DESIGN OF WIDEBAND INTEGRATOR ON FPGA



Vibhooti Sinha
Bandwidth Efficient Design
of Wideband Integrator on
FPGA
A Computer Arithmetic Approach Empowering Your
Implementation of Digital Filter



Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | A Computer Arithmetic Approach Empowering You Implementation of Digital Filter | The goal of this research work is to implement a digital integrator having large bandwidth and high accuracy. we optimized the digital implementation of integrator on virtex5 (xc5vxl110t -1). The operating range of a digital wideband integrator is from 0 to rad/sample. For accurate integration of random signal, a large analog bandwidth (in unit of rad/second) integrator is needed. In order...

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- Authored by Sinha, Vibhooti
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