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SYNTHESIS OF ASYNCHRONOUS CIRCUITS FROM SIMULINK SPECIFICATIONS

Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | An Approach to Asynchronous System Design from an Algorithmic Specification | This work introduces an approach to automatically synthesize Simulink diagrams into asynchronous circuits. It is based on the CodeSimulink co-design environment, a tool developed at Politecnico di Torino able to convert Simulink diagrams into synchronous implementations. Such environment has been extended in two different ways in order to integrate it with conventional FPGA and ASIC flows. The system generated with FPGAs...

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