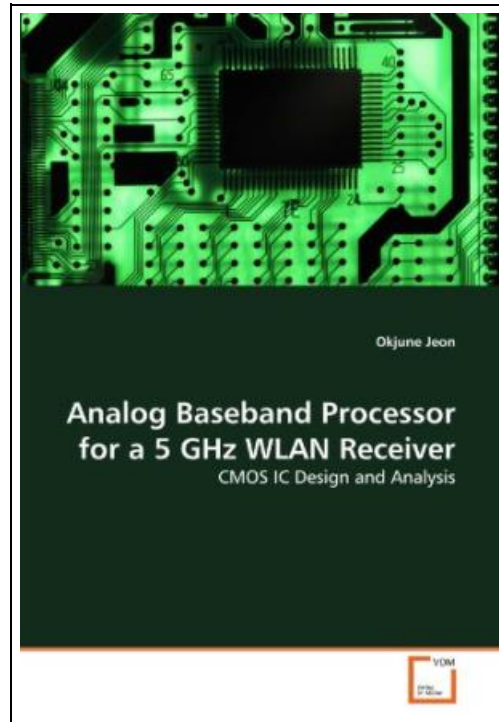# Analog Baseband Processor for a 5 GHz WLAN Receiver

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Reviews

This book is fantastic. This is certainly for all those who statte there had not been a really worth reading. It is extremely difficult to leave it before concluding, once you begin to read the book.
(Prof. Dale Fahey MD)

ANALOG BASEBAND PROCESSOR FOR A 5 GHZ WLAN RECEIVER

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Condition: New. Publisher/Verlag: VDM Verlag Dr. Müller | CMOS IC Design and Analysis | Wireless technologies are progressing rapidly and becoming essentials in our life environment such as smart-phones, mobile tablet pads, and laptop computers. Increasing demand for data network connectivity has made wireless local area network (WLAN) popular. IC design in CMOS process technology also has been the key for providing low-cost single chip solutions to the mobile data communications devices. This book discusses the design of a CMOS analog baseband processor including channel-select filtering with automatic gain control (AGC) for a 5- GHz CMOS WLAN receiver. A new open-loop analog gain- control algorithm for OFDM is proposed. Channel- select filters are composed of 3rd and 4th order cascaded elliptic lowpass Gm-C filters. The AGC circuit is implemented in a 0.18 μm CMOS process using newly designed circuits including linear VGA, RMS detector, and current-mode computation circuitry. The IC design techniques and ideas in the book should be helpful to anyone in the area of CMOS analog circuit design and can be useful to the junior engineers or graduate students in the field. | Format: Paperback | Language/Sprache: english | 116 pp.

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