Research Infrastructures for Hardware Accelerators (Paperback)

By Yakun Sophia Shao, David Brooks

Morgan Claypool Publishers, United States, 2015. Paperback. Condition: New. Language: English . Brand New Book ***** Print on Demand *****. Hardware acceleration in the form of customized datapath and control circuitry tuned to specific applications has gained popularity for its promise to utilize transistors more efficiently. Historically, the computer architecture community has focused on general-purpose processors, and extensive research infrastructure has been developed to support research efforts in this domain. Envisioning future computing systems with a diverse set of general-purpose cores and accelerators, computer architects must add accelerator-related research infrastructures to their toolboxes to explore future heterogeneous systems. This book serves as a primer for the field, as an overview of the vast literature on accelerator architectures and their design flows, and as a resource guidebook for researchers working in related areas.

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