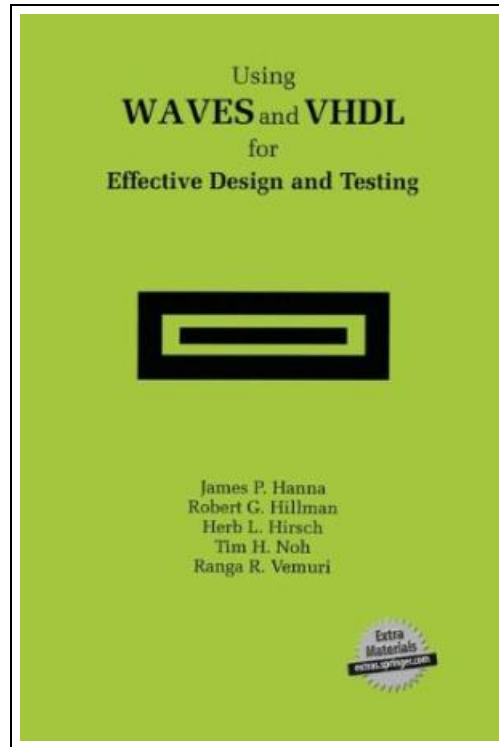# Using WAVES and VHDL for Effective Design and Testing A practical and useful tutorial and application guide for the Waveform and Vector Exchange Specification WAVES

Using
WAVES and **VHDL**
for
Effective Design and Testing

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Reviews

Thorough manual for publication fanatics. It is actually rally intriguing throgh reading through period of time. Its been written in an remarkably simple way and is particularly only after i finished reading through this book in which actually transformed me, change the way i think.
(Morris Schultz)

USING WAVES AND VHDL FOR EFFECTIVE DESIGN AND TESTING A PRACTICAL AND USEFUL TUTORIAL AND APPLICATION GUIDE FOR THE WAVEFORM AND VECTOR EXCHANGE SPECIFICATION WAVES

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Springer. Hardcover. Book Condition: New. Hardcover. 304 pages. Dimensions: 9.4in. x 6.3in. x 1.0in.The proliferation and growth of Electronic Design Automation (EDA) has spawned many diverse and interesting technologies. One of the most prominent of these technologies is the VHSIC Hardware Description Language, or VHDL. VHDL permits designers of digital modules, components, systems, and even networks to describe their designs both structurally and behaviorally. VHDL also allows simulation of the designs in order to investigate their performance prior to actually implementing them in hardware. Having gained the ability to simulate designs once encoded in VHDL, designers were naturally confronted with the issue of testing these designs. VHDL did not explicitly address the requirement to insert particular digital waveforms, often termed test vectors or patterns, or to subsequently assess the correctness of the response from some digital entity. In a distributed design environment, or even in an isolated one where the design was subject to review or scrutiny by another organization, de-facto methods of testing and evaluating results proved faulty. The reason was a lack of standardization. When organization A designed a circuit and tested it with their self-developed test tools it had a certain behavior. When it was delivered to organization B and B tested it using their test tools, the behavior was different. Was the fault in the circuit, in As tools, or in Bs tools The only way to resolve this was for both organizations to agree on a test apparatus, validate its correctness and use it consistently. While VHDL was an IEEE standard language, and consistency among myriad designers was fairly well guaranteed, no such standard existed for test waveform generation and assessment. Hence, the value of standardization in the design language was being negated by the lack of such a standard for testing. The Waveform and...

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