## Find PDF

## SYSTEM-LEVEL MODELLING AND DESIGN SPACE EXPLORATION FOR MULTIPROCESSOR EMBEDDED SYSTEM-ON-CHIP ARCHITECTURES (PAPERBACK)



Vossiuspers Uva, Netherlands, 2007. Paperback. Condition: New. Language: Dutch. Brand New Book \*\*\*\*\* Print on Demand \*\*\*\*\*.Modern embedded systems come with contradictory design constraints. On one hand, these systems often target mass production and battery-based devices, and therefore should be cheap and power efficient. On the other hand, they still need to show high (sometimes real-time) performance, and often support multiple applications and standards which requires high programmability. This wide spectrum of design requirements leads to complex heterogeneous System-on-Chip...

Download PDF System-Level Modelling and Design Space Exploration for Multiprocessor Embedded System-on-Chip Architectures (Paperback)

- · Authored by Cagkan Erbas
- Released at 2007



Filesize: 9.32 MB

## Reviews

Absolutely essential go through publication. It is filled with knowledge and wisdom Once you begin to read the book, it is extremely difficult to leave it before concluding.

-- Dr. Sierra Lowe Sr.

It in one of the most popular publication. It is actually writter in easy words instead of confusing. You will like how the author create this book.

-- Art Gislason

If you need to adding benefit, a must buy book. It is packed with wisdom and knowledge I am just effortlessly could get a pleasure of reading a written publication.

-- Lea Legros V